

FIG. 1

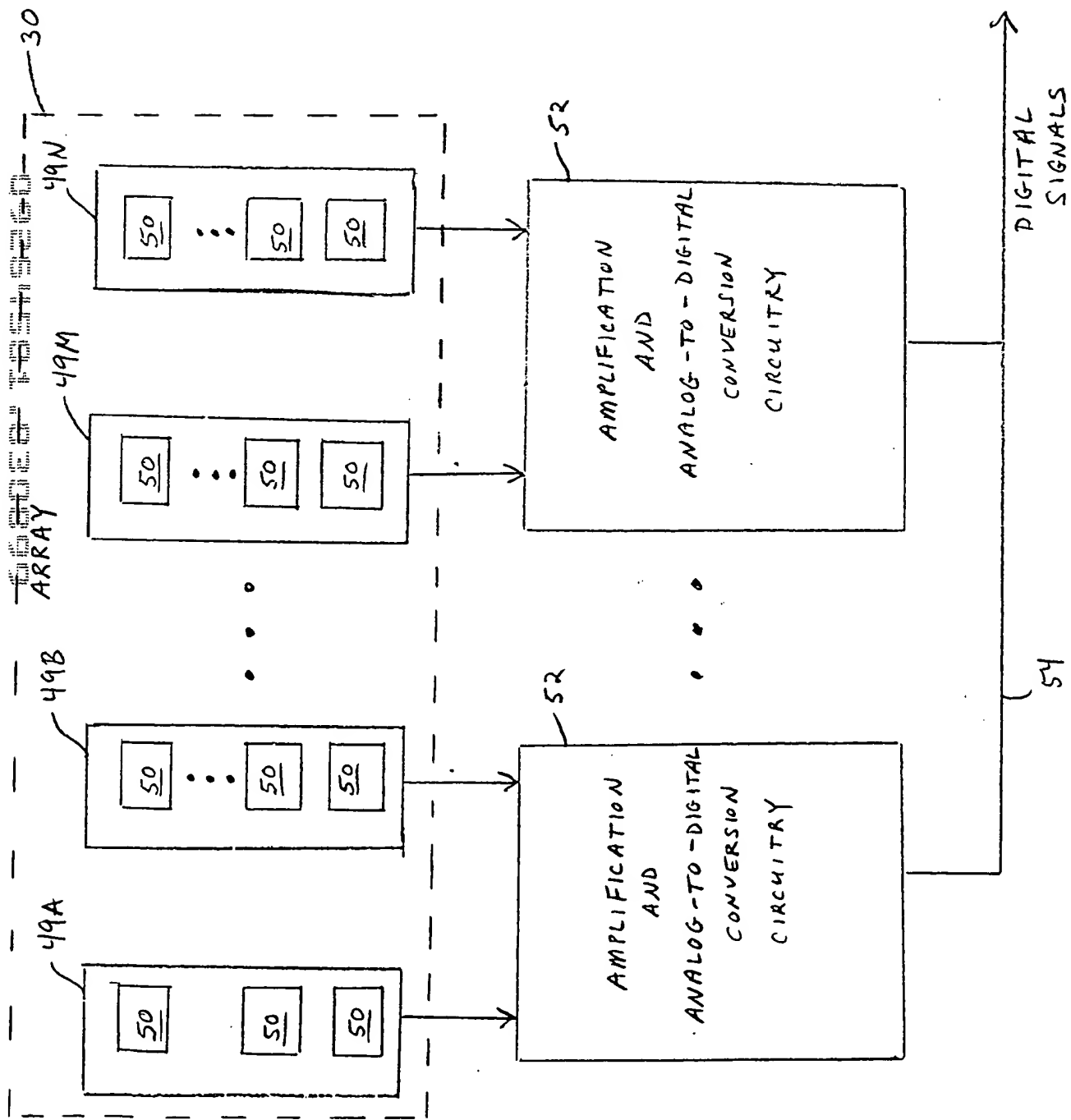


FIG. 2

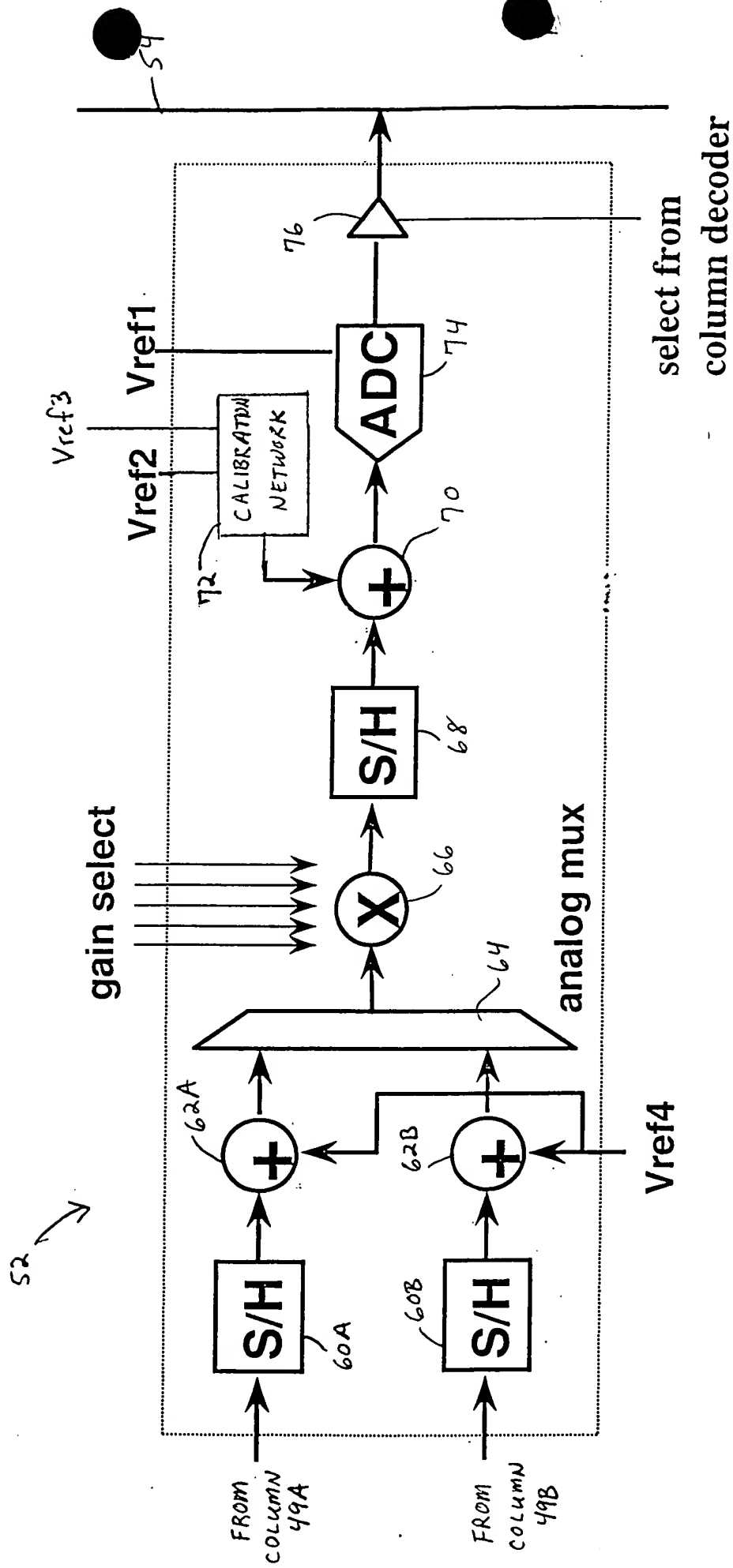


FIG. 3

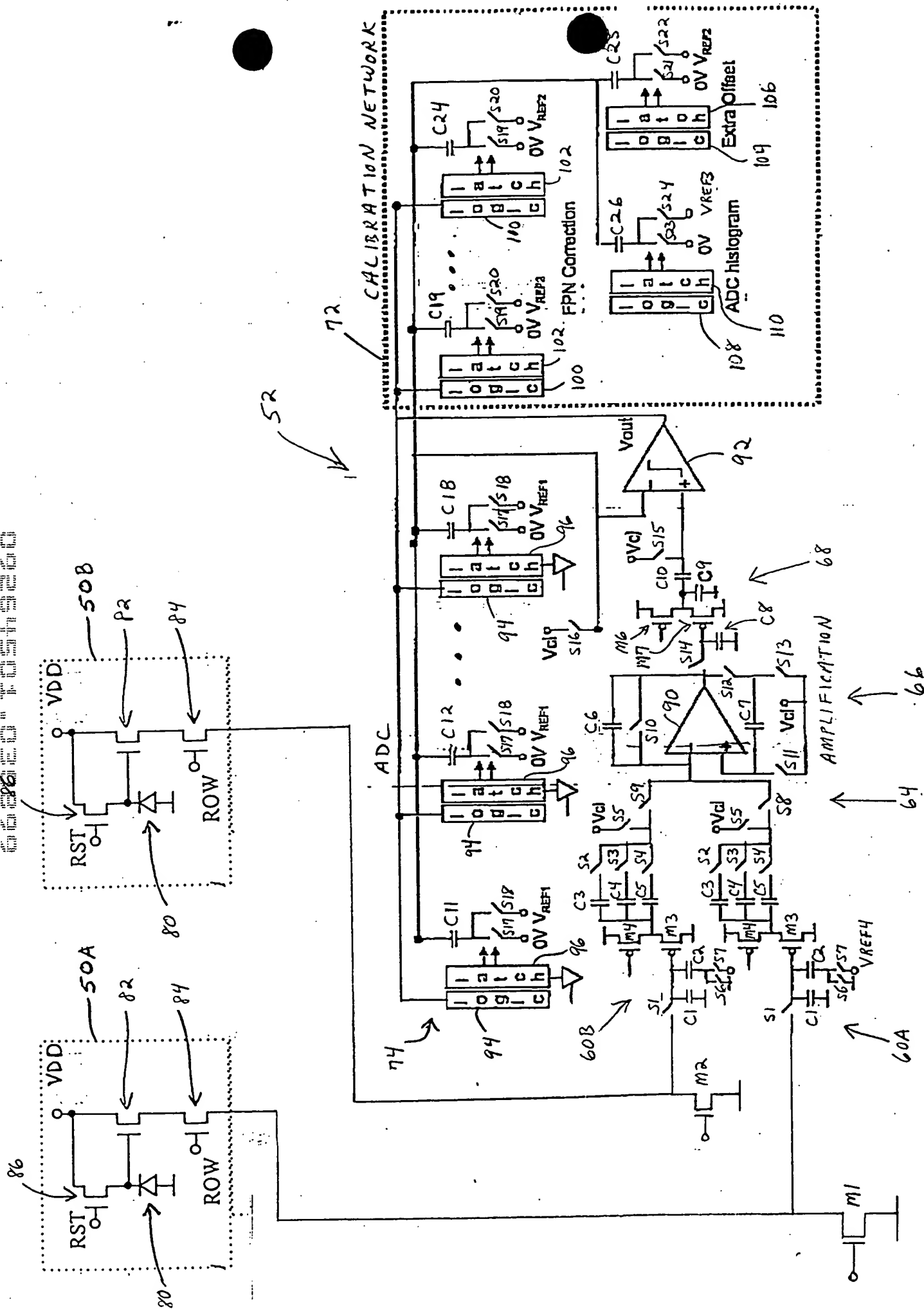
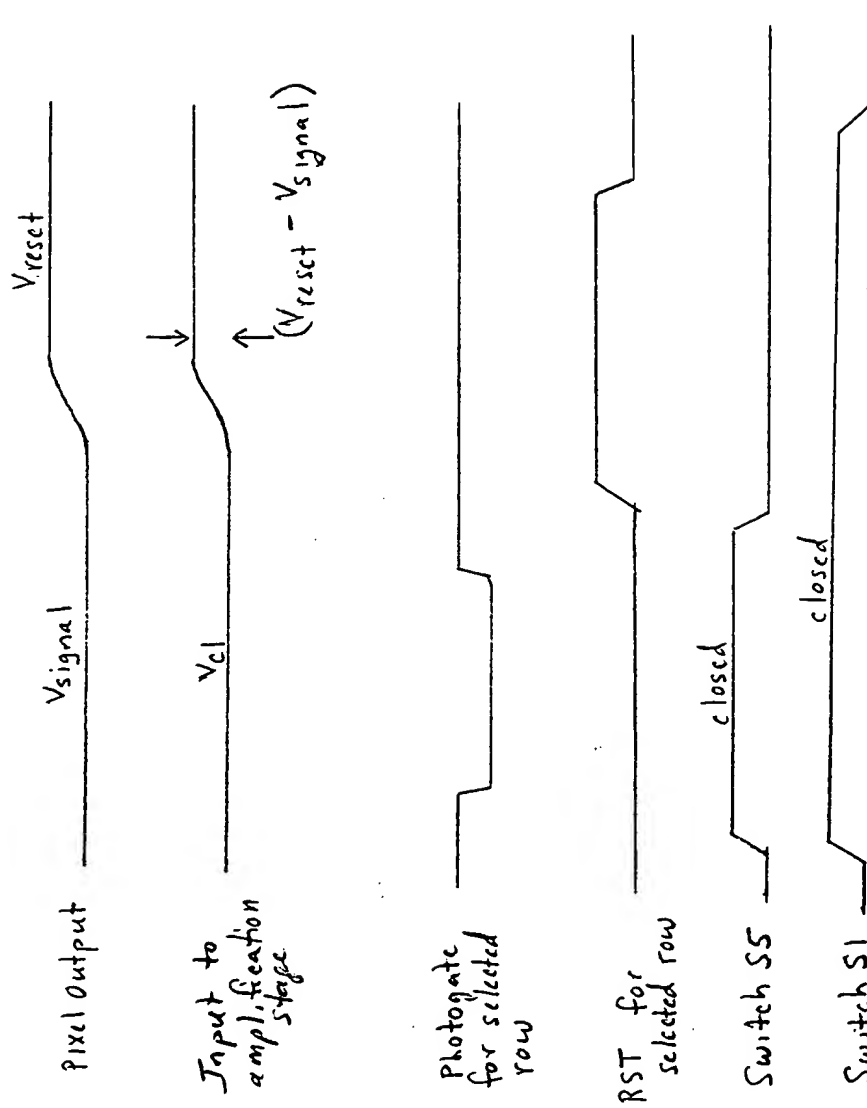


FIG. 4



SAMPLE AND HOLD TIMING

FIG. 5

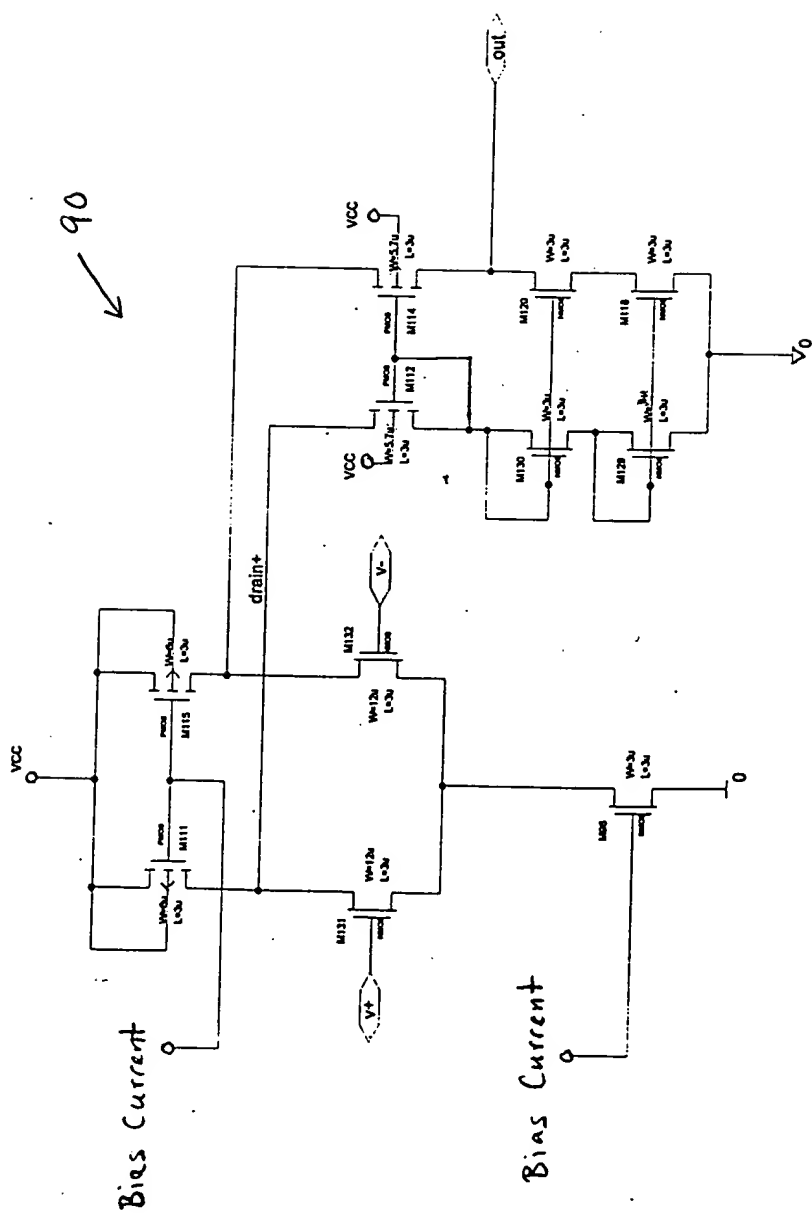
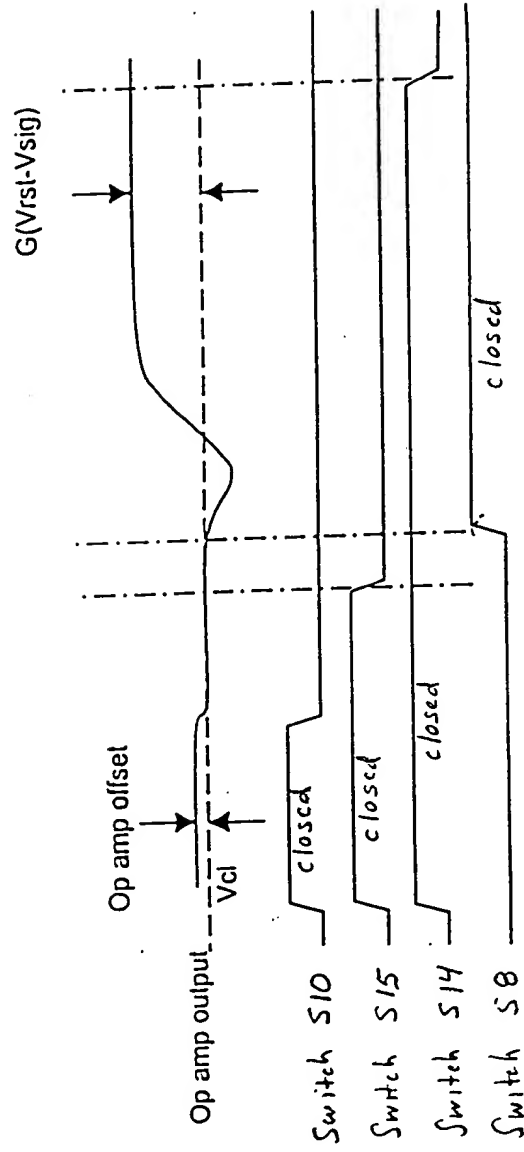


FIG. 6



Amp lification Timing

FIG. 7

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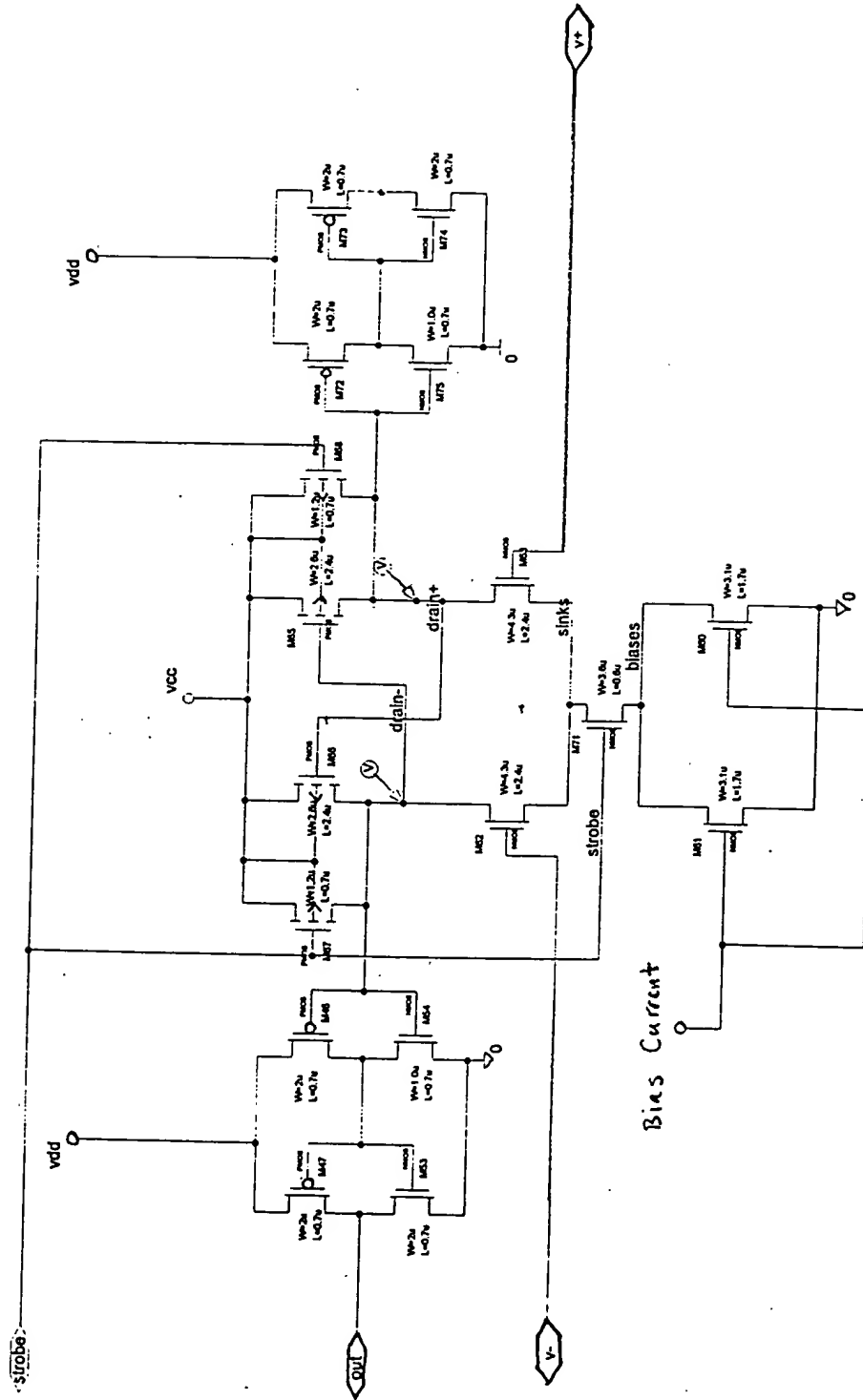


FIG. 8



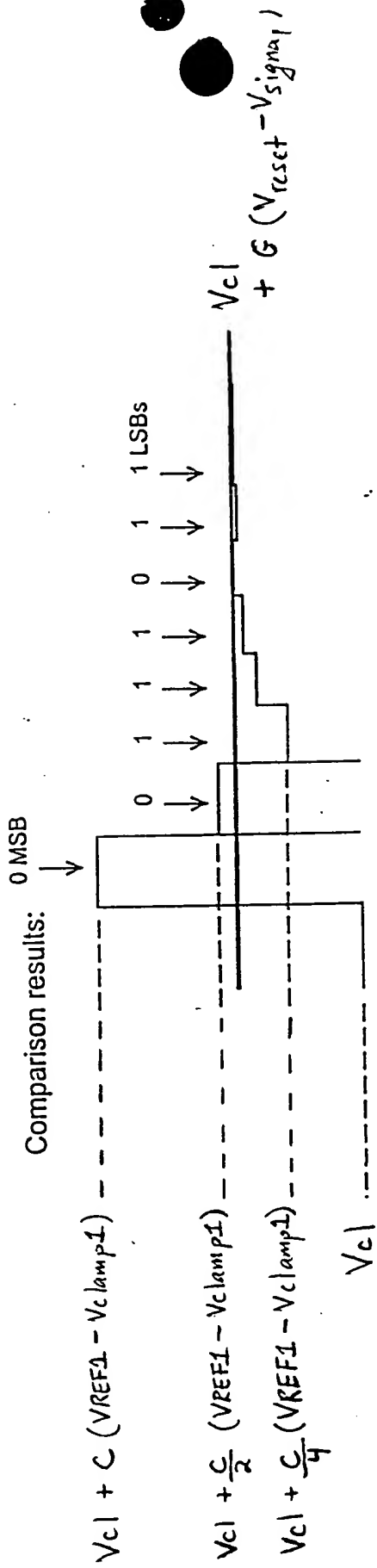


FIG. 9

To negative terminal  
of comparator q2

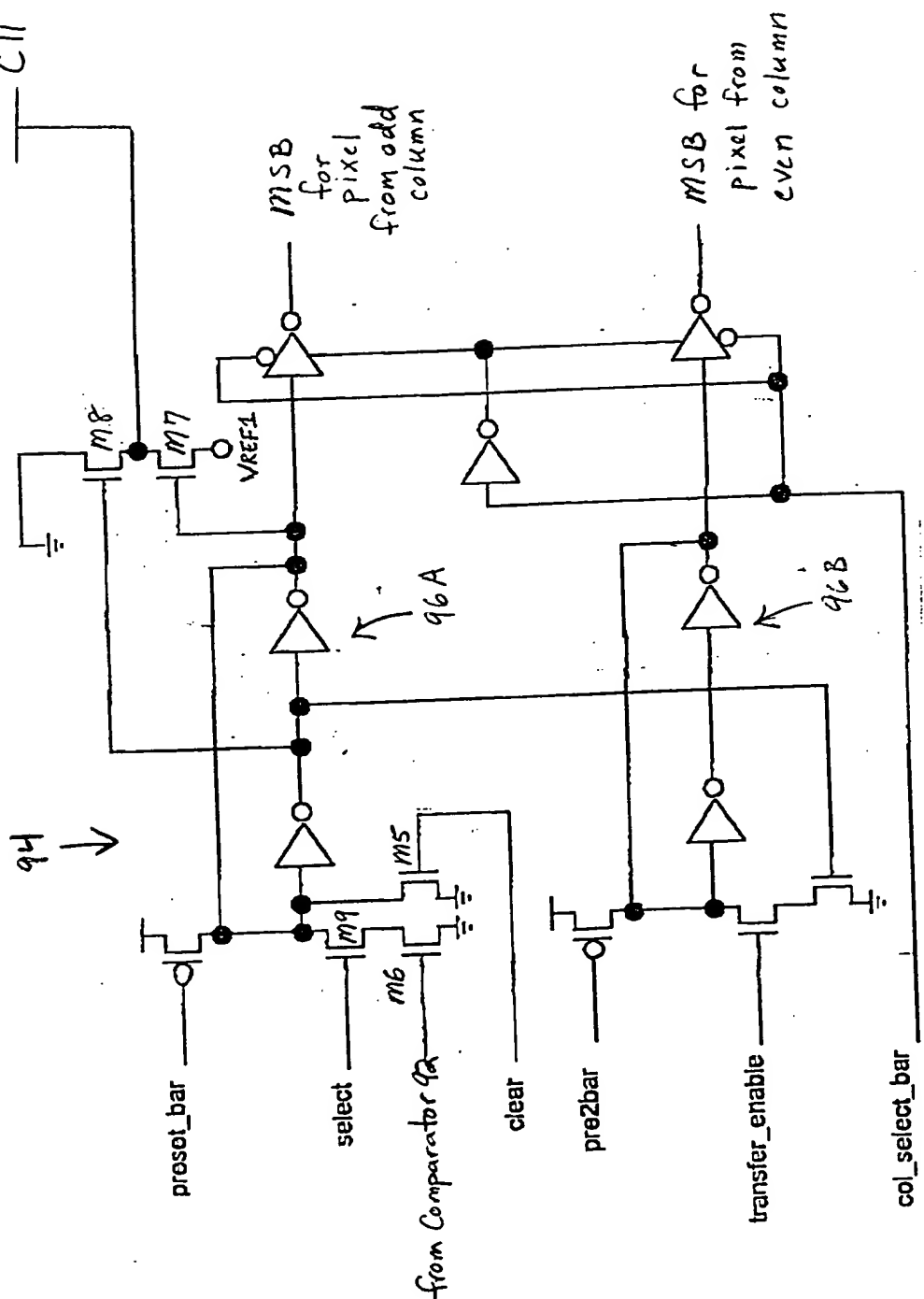
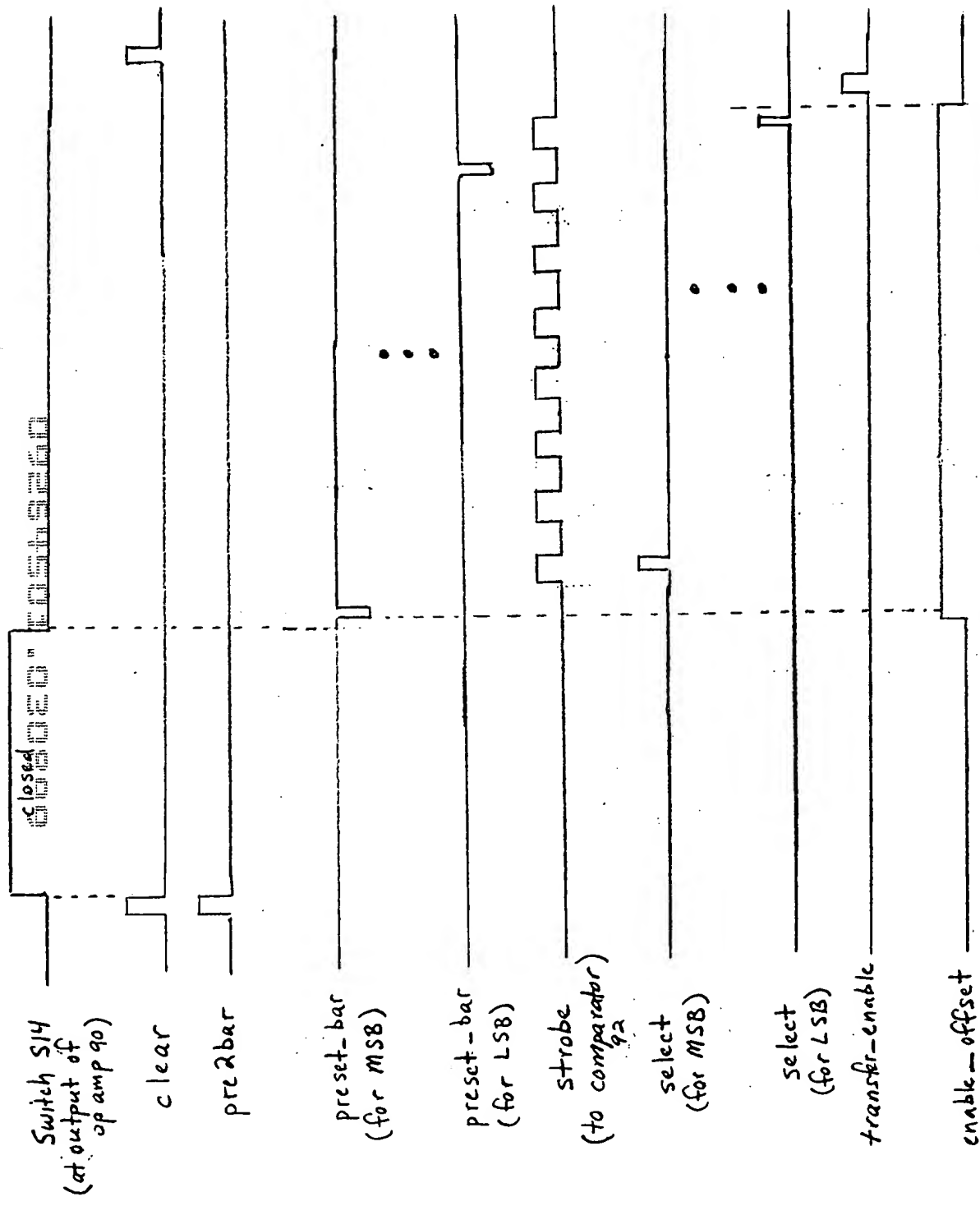


FIG. 10



← AMPLIFICATION → ANALOG-TO-DIGITAL CONVERSION

FIG. 11

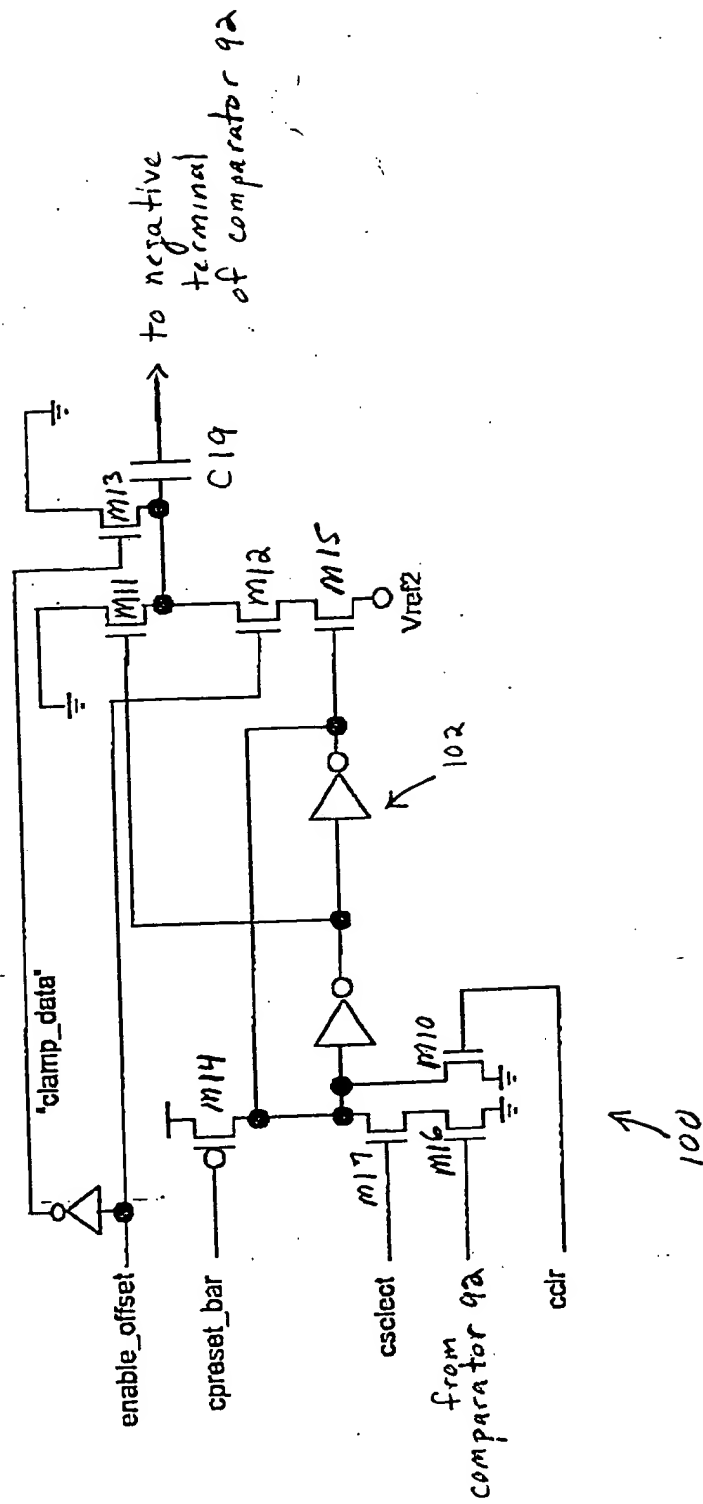
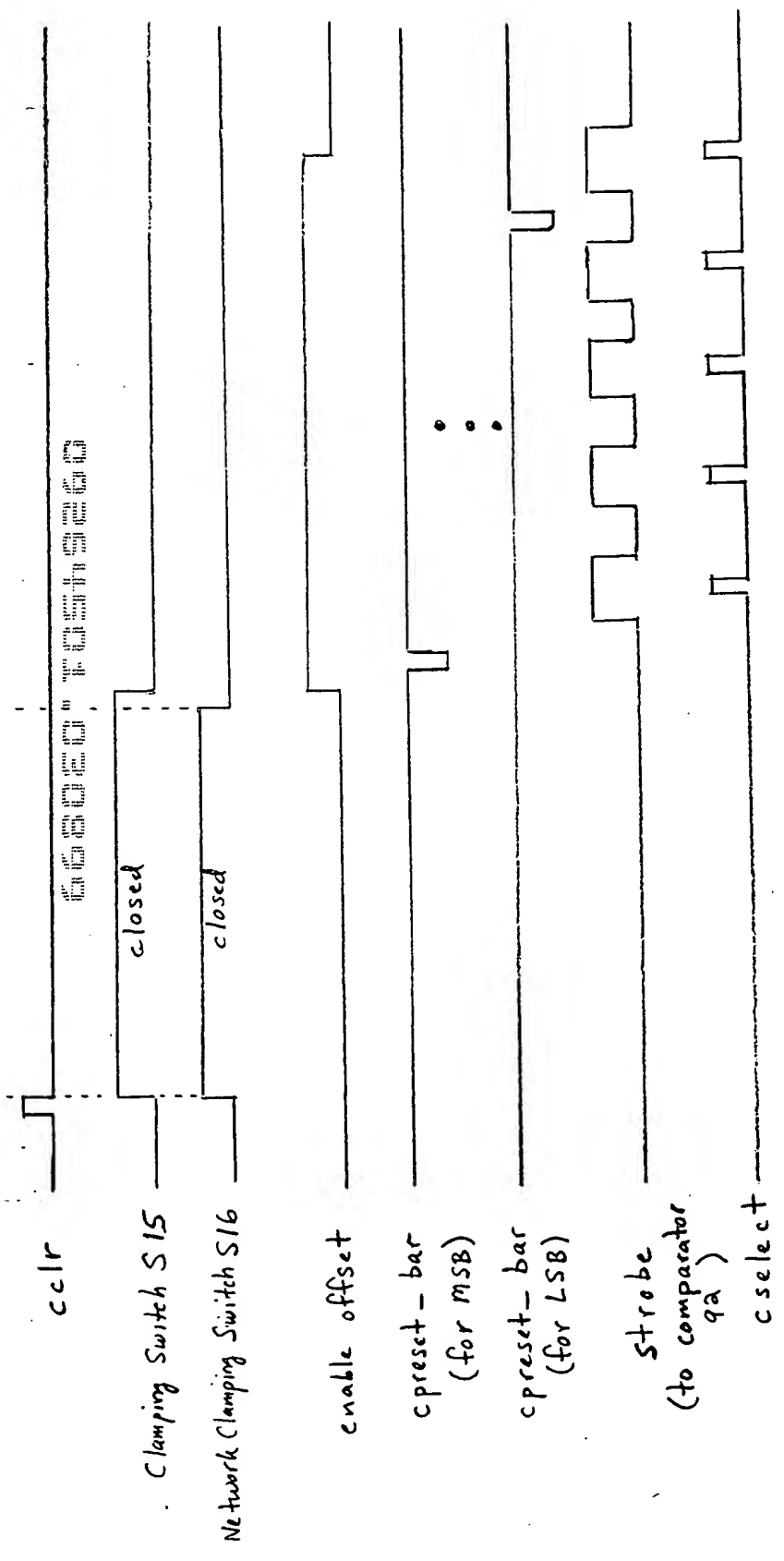
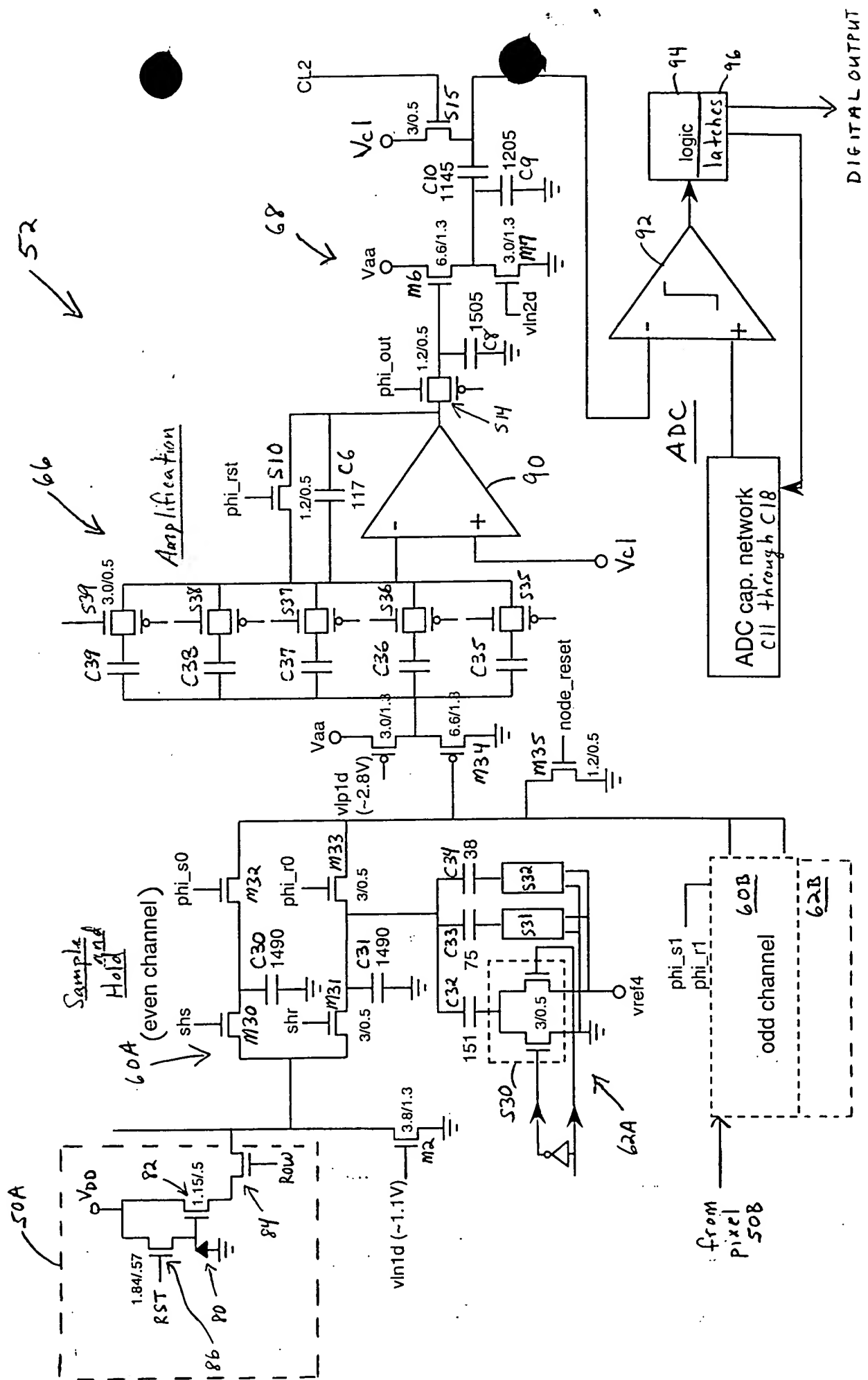


FIG. 12



CALIBRATION TIMING

FIG. 13



# pixel timing

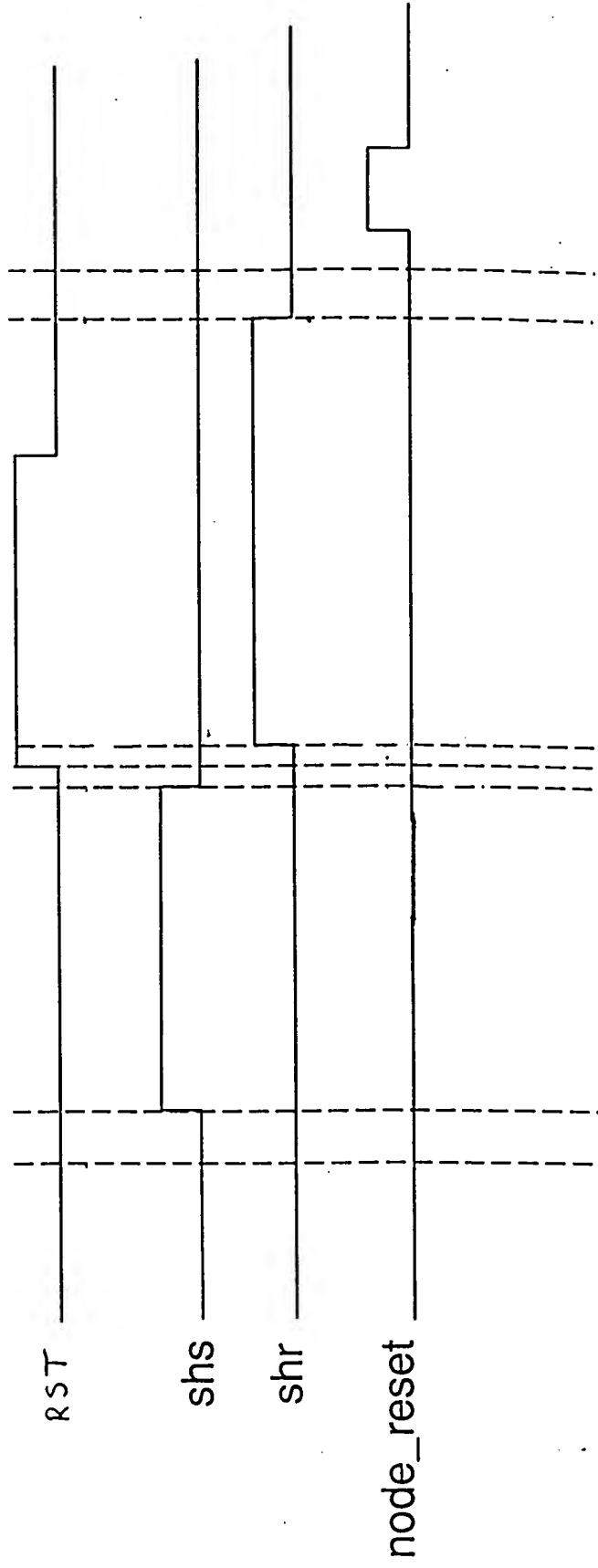


FIG. 15

# gain timing

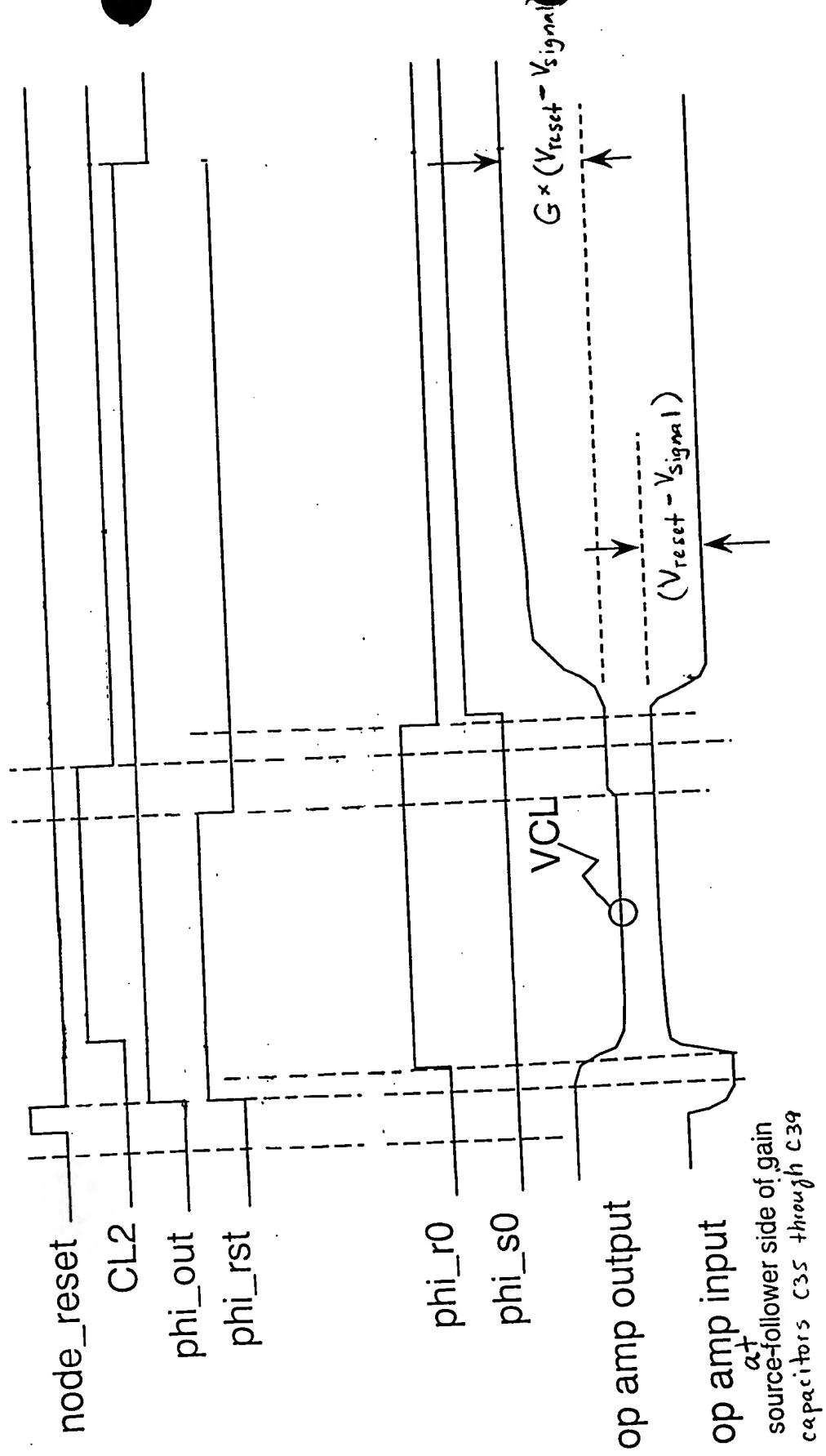


FIG. 16